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M122_1544 09/677478 Sheet 1 of 4

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1544	SERIAL NO. 09/677,478		
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Blaetck et al.			
				FILING DATE October 3, 2000	GROUP 1765		
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,533,053 B2 <i>6533053</i>	02/18/03	Allen, III <i>Allen, III</i>			
	AB	5,304,353 <i>5566353</i>	02/09/99	Chen et al. <i>Chen et al.</i>			
	AC	5,855,936 <i>5365936</i>	02/02/99	Peters et al. <i>Peters et al.</i>			
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	AE	5,026,646 <i>5026646</i>	06/25/91 <i>06/25/91</i>	Hills et al. <i>Hills et al.</i>			
	AF	6,434,327 B1 <i>6434327</i>	08/13/02	Gronet et al. <i>Gronet et al.</i>			
	AG		<i>8/13/02</i>				
	AH						
	AI						
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation Yes No
	AL						
	AM						
	AN						
	AO						
	AP						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)							
	AR	Stanley Wolf and Richard Tauber. Silicon Processing for the VLSI Era. Vol. 1, pp 549. 1986.					
	AS						
	AT						
EXAMINER				DATE CONSIDERED			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with 37CFR 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							